



### 3.5-year BEng (CE) Accelerated Bachelor Programme (ABP)

*Applicable to students matriculated in 2018 or later*

This accelerated programme is not applicable to polytechnic students with exemptions. Students under ABP have to do Professional Internship (PI).

### Overview of AUs requirement

Year of Study	Core	Major Prescribed Elective (MPE)	General Education Requirement (GER)				Unrestricted Elective (UE)	Total	
			Core	Prescribed Elective (PE)					
				BM	LA	STS			
1	Sem 1	14		3			3	20	
	Sem 2	15		6				21	
2	Sem 1	15		2			3	20	
	Sem 2	9	3		3	3		21	
3	Sem 1	10						10	
	Sem 2	7	6	3			6	22	
4	Sem 1	14	9				3	26	
<b>Total</b>		84	18	14	3	3	3	15	140
				23					

### Suggested Curriculum

Course Code and Title	Type (i.e. Core/ MPE/ GER core/ GER PE/ Unrestricted elective)	No of Hours Per Week				AU	Pre-requisite / Remarks
		Lec / TEL*	Tut	Lab/ Example class <sup>†</sup>	Total		
<b>YEAR 1 SEMESTER 1</b>							
CE1011 Engineering Mathematics I	Core	2	1	1 <sup>†</sup>	4	3	-
CE1012 Engineering Mathematics II	Core	2	1	1 <sup>†</sup>	4	3	CE1011 (can be taken concurrently)
CE1003 Introduction to Computational Thinking	Core	2*	0	2 <sup>†</sup>	4	3	-
CE1013 Physics for Computing	Core	2	0	2	3	2	-
CE1005 Digital Logic	Core	2	1	1	4	3	-
Science & Technology	GER PE - STS	2	1	-	3	3	
HW0188 <sup>^</sup> Engineering Communication I	GER core	-	2	-	2	2	HW0001 (can be taken concurrently)



GC0001 Sustainability: Seeing Through The Haze	GER core					1	online course
HW0001 English Proficiency	GER core					0	
<b>TOTAL</b>		<b>10+2*</b>	<b>6</b>	<b>3+4*</b>	<b>24</b>	<b>20</b>	
<b>YEAR 1 SEMESTER 2</b>							
CE1006 Computer Organisation and Architecture	Core	2	1	1	4	3	CE1005 (can be taken concurrently)
CE1007 Data Structures	Core	2	1	1	4	3	CE1003
MH1812 Discrete Mathematics	Core	2	1	1 <sup>+</sup>	3	3	
CE2003 Digital Systems Design	Core	2	1	1	4	3	CE1005
CE2004 Circuits and Signal Analysis	Core	2	1	1	4	3	CE1012
CE0001 Engineers and Society <sup>#</sup>	GER core	2	1	-	3	3	
CE1015 Introduction to Data Science and Artificial Intelligence	GER core	2*	0	2	4	3	
<b>TOTAL</b>		<b>12+2*</b>	<b>7</b>	<b>6+1*</b>	<b>26</b>	<b>21</b>	

<sup>^</sup> Should there be insufficient vacancies, students will take Eng Comm I & II in the following semester.

<sup>#</sup> Available only in semester 2. Should there be insufficient vacancies, students will take Engrs and Society in the next semester 2.

Course Code and Title	Type (i.e. Core/ MPE/ GER core/ GER PE/ Unrestricted elective)	No of Hours Per Week				AU	Pre-requisite / Remarks
		Lec / TEL*	Tut	Lab/ Example class <sup>+</sup>	Total		
<b>YEAR 2 SEMESTER 1</b>							
CE2001 Algorithms	Core	2	1	1 <sup>+</sup>	4	3	CE1007, CE1012, MH1812
CE2002 Object Oriented Design & Programming	Core	2	1	1	4	3	CE1007
CE2005 Operating Systems	Core	2	1	1	4	3	CE1006, CE1007
CE2007 Microprocessor-based Systems Design	Core	2	1	1	4	3	CE1006, CE2004 (can be taken concurrently)
CE3002 Sensors, Interfacing and Control	Core	2	1	1	4	3	CE2004
HY0001 Ethics & Moral Reasoning	GER core					1	online course
ML0003 Professional Development Course	GER core					1	online course
Unrestricted elective	Unrestricted elective	2	1	-	3	3	
<b>TOTAL</b>		<b>12</b>	<b>6</b>	<b>4+1*</b>	<b>23</b>	<b>20</b>	



YEAR 2 SEMESTER 2							
CE2006 Software Engineering	Core	2	1	1	4	3	CE2002 (can be taken concurrently)
CE3001 Advanced Computer Architecture	Core	2	1	1 <sup>+</sup>	4	3	CE1006
CE3005 Computer Networks	Core	2	1	1	4	3	CE1011, CE1012
Technical Elective 1 (CZ200x or CZ300x)	MPE	2	1	1	4	3	
Liberal Arts	GER PE - LA	2	1	-	3	3	
Business & Management	GER PE - BM	2	1	-	3	3	
Unrestricted elective	Unrestricted elective	2	1	-	3	3	
<b>TOTAL</b>		<b>14</b>	<b>7</b>	<b>3+1<sup>+</sup></b>	<b>25</b>	<b>21</b>	

Course Code and Title	Type (i.e. Core/ Core elective/ GER core/ GER elective/ Unrestricted elective)	No of Hours Per Week				AU	Pre-requisite / Remarks
		Lec	Tut	Lab	Total		
<b>YEAR 3 SEMESTER 1</b>							
CE3179 Professional Internship	Core	-	-	-	-	10	ML0003, Year 3 standing
<b>TOTAL</b>		-	-	-	-	<b>10</b>	
<b>YEAR 3 SEMESTER 2</b>							
CE4079 Final Year Project	Core	-	-	-	-	-	Final year standing
CE3003 Microcontroller Programming	Core	2	1	1	4	3	CE2005
CE3004 Multidisciplinary Design Project	Core	1	-	3	4	4	Year 3 standing
CE4xxx Technical Elective 2	MPE	2	1	1	4	3	
CE4xxx Technical Elective 3	MPE	2	1	1	4	3	
ET0001 Enterprise & Innovation	GER core					1	online course
HW0288 <sup>^</sup> Engineering Communication II	GER core	-	2	-	2	2	HW0188, Year 3 standing
Unrestricted elective	Unrestricted elective	2	1	-	3	3	
Unrestricted elective	Unrestricted elective	2	1	-	3	3	
<b>TOTAL</b>		<b>11</b>	<b>7</b>	<b>6</b>	<b>24</b>	<b>22</b>	



<b>YEAR 4 SEMESTER 1</b>							
CE4079 Final Year Project	Core	-	-	-	-	8	
CE3006 Digital Communications	Core	2	1	1	4	3	CE1011, CE2004
CE3007 Digital Signal Processing	Core	2	1	1	4	3	CE2004
CE4xxx Technical Elective 4	MPE	2	1	1	4	3	
CE4xxx Technical Elective 5	MPE	2	1	1	4	3	
CE4xxx Technical Elective 6	MPE	2	1	1	4	3	
Unrestricted elective	Unrestricted elective	2	1	-	3	3	
<b>TOTAL</b>		<b>12</b>	<b>6</b>	<b>5</b>	<b>23</b>	<b>26</b>	
<b>GRAND TOTAL (Year 1 to 3.5)</b>						<b>140</b>	

+ An additional 10 hours per semester have been allocated for these non-lab based courses for Example Classes. Faculty can use these time slots for their pedagogical needs such as to reinforce concepts, provide additional coaching, give more worked examples, allow students to do practice exercises or do research or work on the computers etc.