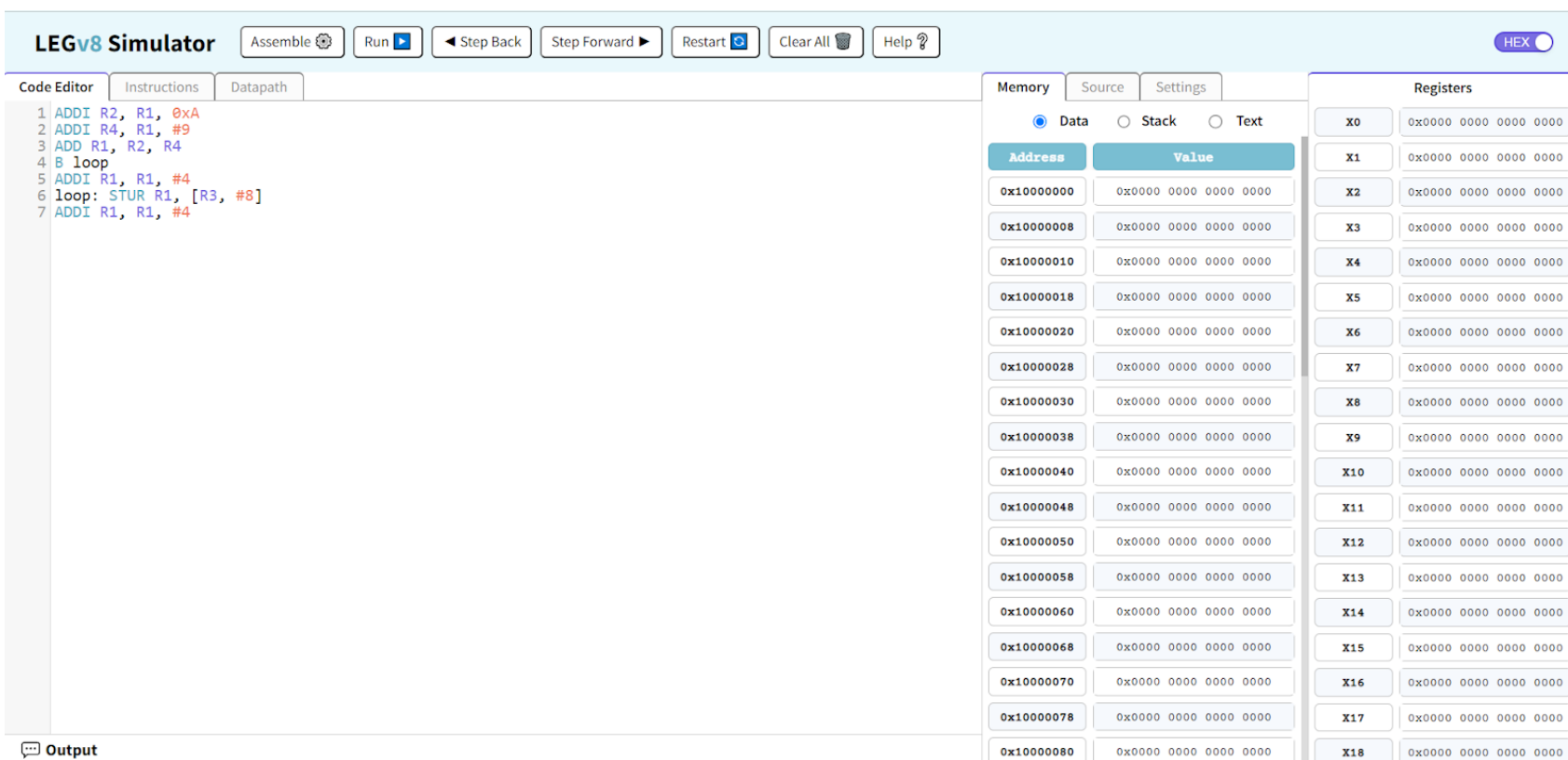


# Educational simulator

## For analysing Pipelined LEGv8 architecture

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### Features

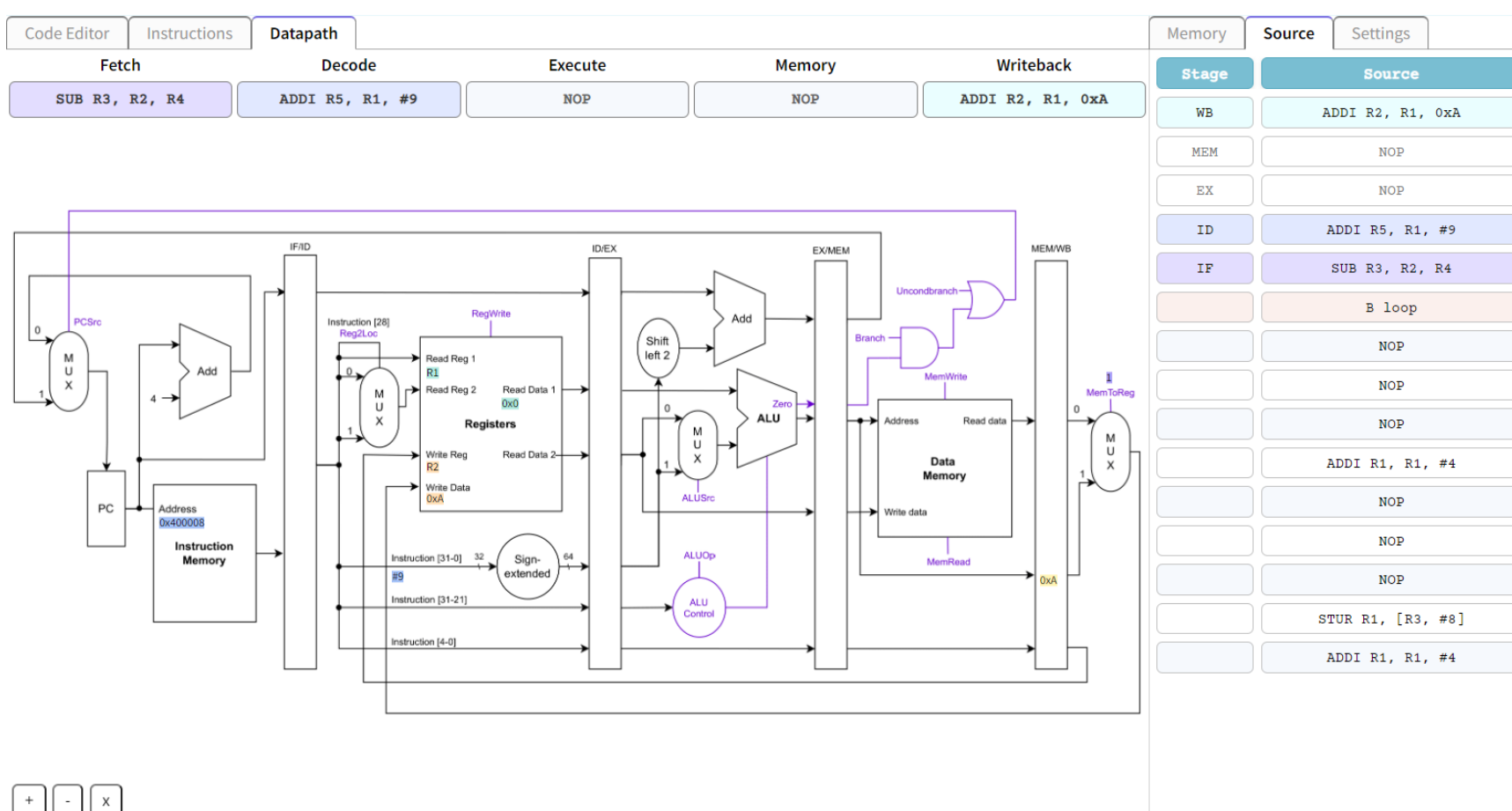
- Code Editor
- Assembler
- Single Cycle & Pipelined Execution
- Pipeline Hazard Detecting & Handling
- Datapath Visualisation
- Error Checking & Logging

### Motivation

As computer architecture concepts are abstract and complex, there is a need for graphical, intuitive, and flexible teaching aids to supplement students' learning. However, there are currently no educational simulators built for the LEGv8 architecture.

Address	Instruction	Label	Source	Meaning
[W] 0x400000	0x22008CA5		ADDI x5, x5, 23	x5 = x5 + 35
[M]	NOP		NOP	
[E]	NOP		NOP	
[D] 0x400004	0xF8408041		LDUR x1, [x2, #8]	x1 = Memory[x2 + #8]
[F]	NOP		NOP	
	NOP		NOP	
0x400008	0xCB050024		SUB x4, x1, x5	x4 = x1 - x5
0x40000c	0x8A070026		AND x6, x1, x7	x6 = x1 & x7

Pipelined execution of assembled instructions



Datapath Visualisation during Pipelined instruction execution

### Objective

Develop a **web application** that can be used by student to **enhance their learning and understanding of computer architecture** by providing them with a **user-friendly and interactive** medium to explore and learn the basic set of instructions that is supported by the **LEGv8 architecture**