Chip Gallery

- Low-Power High Dynamic Range Imaging Transducer IC for Portable Electrical Capacitance Tomography
- Low-Jitter Polyphase Based Frequency Multiplier for High-Speed ADC
- First Employing Chopper-Stabilization in Differential Difference Amplifier for Biomedical Instrumentation IC
- Low-Power High-Resolution Capacitive Transducer IC for Highly Sensitive Sensors
- High Power-Bandwidth Efficient CMOS Amplifier IC for Heavy Capacitive Load with Small Compensation Capacitor
- Wideband Low-Distortion CMOS Headphone Amplifier IC
- Low-Quiescent High-PSR Voltage Regulator IC for Sensors

<Dr Pak Kwong CHAN>
<Contact: epkchan@ntu.edu.sg>
Chip Gallery

65nm CMOS Output Capacitorless Low Dropout Regulator with only 0.9μA Quiescent Current

65nm CMOS Operational Amplifier Driving 500pF Capacitive Load with only 1.15pF Compensation Capacitor
Related Research Publications

Sensor Interface ICs


Related Research Publications

**Analog/Mixed-Signal ICs**


Related Research Publications
Amplifier ICs
Related Research Publications

Power Management ICs


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**Graduate Students**

**List of Graduated PhD Students**


List of Graduated MEng Students


List of Graduated MSc Students

7. Sravanthi Vullikanti, “Design of an Ultra Low-Power Interface Circuit for MEM-Based Accelerometer IC” MSc Thesis 2007. (NTU-TUM Joint MSc)
List of Current Postgraduate Students

1. Wang Yanmei  (PhD)    On-going  
2. Tan Xiaoliang  (PhD)   On-going  
3. Koay Kuan Chang  (PhD)   On-going  
4. Wang Dong  (PhD)   On-going  
5. Xiao Fei  (MEng)  On-going  

Current Job Position of Former Research Students

• Chen Deyu   Senior Staff Scientist   Broadcom, Singapore
• Chong Sau Siong  Design Engineer   Infineon Technologies Asia Pacific Pte Ltd.
• Chuah Yew Tat  Principal Member of Technical Staff  Maxim Integrated, Singapore
• Cui Jie   IPR and Patent Manager  Infineon Technologies Asia Pacific Pte Ltd.
• Fan Xian Ping  Principal Engineer  O2 Micro, Singapore
• Hendra Kwantono  Design Engineer  Broadcom, Singapore
• Iulian Mirea  Principal Engineer  Broadcom, U. S. A.
• Lee Wing Foon  Associate Professor  University of Electronic Science and Technology of China
• Lee Guan Boon  Senior IC Design Engineer  ST Microelectronics, Singapore
• Mukesh Rao Engla Syam  Senior Design Engineer  ON Semiconductor, India
• Ong Geok Teng  Senior Staff Engineer  MediaTek, Singapore
• Peng Jia  Principal Engineer  Avago Technologies, Singapore
• Ng Kian Ann  Research Fellow  Singapore Institute of Neurotechnology
• Sravanthi Vullikanti  Design Engineer  Texas Instruments, India
• Tay Hwee Chua  Director  MEDs IC Solutions Pte Ltd. (MOSIS Asia Pacific HQ Office)
• Tian Yang  Research Assistant  Temasek Laboratories @ NTU
• Yin Jee Khoi  Scientist  Institute of Infocomm Research
• Zhang Hong Liang  Director  Luminex Pte Ltd. Singapore
• Zhang Hui Yuan  IC Design Engineer  Panasonic Semiconductor Asia Pte Ltd.
• Zhang Xiao Ling  Principal Engineer  GLOBALFOUNDRIES