Model Predictive Control (MPC) on a Chip (022-106-0044)
PI: Dr Ling Keck Voon

Brief Description on Progress/Achievements:
Model Predictive Control (MPC) outperforms other control strategies through its ability to deal with constraints. This requires on-line optimisation, hence computational complexity can become an issue when applying MPC to complex systems with fast response times. Motivated by the need for a scalable and low-cost embedded MPC algorithm, we proposed a time-multiplexed strategy which solves the MPC problem for each subsystem sequentially, and updates each subsystem as soon as the solution is available, thus distributing the control moves over a complete update cycle. The resulting computational speed-up allows faster response to disturbances, and hence improved performance, despite finding sub-optimal solutions to the original problem. The multiplexed MPC scheme is also closer to industrial practice in many cases. We also obtained initial stability results for two variants of multiplexed MPC. This gives a theoretical foundation for the proposed multiplexed MPC strategy.

We believe that the time multiplexed version of MPC opens up many opportunities to applying MPC to embedded and high bandwidth applications, such as ‘Lab-on-chip’ devices, precision machines, land and aerial vehicles. Continuing research in this direction to bring the technology to a more matured level is necessary. In addition, we have developed a set to tools which could take a MPC design, simulate it, fine tune the parameters of the MPC algorithm, and encapsulate the final MPC solution as suitable standalone FPGA module for embedded applications.

Publications

Presentations

Manpower Trained
One Postdoc and one PhD student

Joint Programme